1/4 B.Tech - SECOND SEMESTER

BASIC ELECTRONICS ENGINEERING LAB

(Common to CSE, IT during I B.Tech., II Semester)

Course Code: CS2L2, IT2L2 Credits: 2

Internal assessment: 25 marks

Lab: 3 Periods /week Semester end examination: 50 marks

Objectives:

- To study basic electronic components.
- To observe characteristics of electronic devices.
- To get the practical exposer of the Op-amp applications.
- To study the practical limitations of the Op-amp.
- To study Linear & Non linear wave shaping.

Outcomes:

Students will be able

- To apply the concepts and analytical principles to analyze electronic (diodes, transistors) circuits.
- To Understanding of the operation diodes and transistors in order to build circuits.
- To learn to the characteristics of Transistor.
- To learn the basics of Amplifiers.
- The students are able to design Op-amp circuits.

Part I

- 1. The identification & Testing of Electronic component like R,L,C, Diodes, Transistors etc.
- 2. Study of CRO, function generator, regulated power supply etc.,

Part II

Any TEN Experiments

- 1. Diode Characteristics (Si) a) Forward Bias b) Reverse Bias
- 2. Zener Diode Characteristics
- 3. Half Wave rectifier with & without filter
- 4. Full Wave rectifier with & without filter
- 5. Transistor CB Characteristics (I/P & O/P)
- 6. Transistor CE Characteristics (I/P & O/P)
- 7. CE Amplifier
- 8. CC Amplifier
- 9. CB Amplifier
- 10. Op-amp inverting amplifiers (OP -AMP Applications) Adder, Subtractor, Comparator Circuits.
- 11. Op-amp non-inverting amplifiers (OP -AMP Applications) Adder, Subtractor, Comparator Circuits
- 12. Op-amp inverting amplifier for desired gain and bandwidth.

Reference Books:

- 1. Electronic devices & circuits by B.L.Theraja, R.S.Sedha, S.Chand publications
- 2. Electronic devices & circuits by Robert L.Boylested
- 3. Linear Integrated Circuits by D. Roy Chowdhury, New Age International Pvt.Ltd., 2nd Edition, 2003.